	Application No.	Applicant(s)	
Nation of Allered 1994	09/515,807	MERRILL ET AL.	
Notice of Allowability	Examiner	Art Unit	
	Lin Ye	2615	
The MAILING DATE of this communication appe All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this app or other appropriate communication GHTS. This application is subject to and MPEP 1308.	olication. If not include will be mailed in due	ed course. <b>THIS</b>
1. This communication is responsive to the amendment filed of	on 26 August 2004 .		
2. The allowed claim(s) is/are <u>1-10,12-20 and 39-49</u> .			
3. The drawings filed on 29 February 2000 are accepted by the	ne Examiner.		
<ul> <li>4. ☐ Acknowledgment is made of a claim for foreign priority un</li> <li>a) ☐ All b) ☐ Some* c) ☐ None of the:</li> <li>1. ☐ Certified copies of the priority documents have</li> <li>2. ☐ Certified copies of the priority documents have</li> </ul>	been received.		
3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the			
International Bureau (PCT Rule 17.2(a)).			
* Certified copies not received:			
Applicant has THREE MONTHS FROM THE "MAILING DATE" on noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	of this communication to file a reply of ENT of this application.	complying with the red	quirements
5. A SUBSTITUTE OATH OR DECLARATION must be submi INFORMAL PATENT APPLICATION (PTO-152) which give			OTICE OF
6. CORRECTED DRAWINGS ( as "replacement sheets") mus	t be submitted.		
(a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached			
1) 🔲 hereto or 2) 🔲 to Paper No./Mail Date			
(b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date			
Identifying indicia such as the application number (see 37 CFR 1. each sheet. Replacement sheet(s) should be labeled as such in the	84(c)) should be written on the drawin ne header according to 37 CFR 1.121(d	gs in the front (not the ).	back) of
7. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.			
Attachment(s) 1. ☐ Notice of References Cited (PTO-892)	5. D Notice of Informal De	stant Assisstics (DTC	2.450)
2. ☐ Notice of Certerious Cited (FTO-692)  2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)	<ul><li>5. ☐ Notice of Informal Pa</li><li>6. ☑ Interview Summary (</li></ul>		<i>)</i> -152)
	Paper No./Mail Date		,
<ol> <li>Information Disclosure Statements (PTO-1449 or PTO/SB/08         — Paper No./Mail Date</li> </ol>			
4. Examiner's Comment Regarding Requirement for Deposit	8. X Examiner's Statemen	nt of Reasons for Allo	wance
of Biologicai Material	9.		
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## **DETAILED ACTION**

An examiner's amendment to the record appears below. Should the changes and/or additions
be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To
ensure consideration of such an amendment, it MUST be submitted no later than the payment
of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Kenneth D'Alessandro on October 29, 2004.

The application has been amended as follows:

- a. Cancel the claims 22-37.
- 2. The following is an examiner's statement of reasons for allowance:

The prior art does not teach or fairly suggest a plurality of storage pixel sensors disposed on a semiconductor substrate, each of the plurality of storage pixel sensors comprising: a photodiode, reset transistor, a source follower transistor, a capacitive storage node; and an exposure transistor having a source directly connected to the source of the source-follower transistor and a drain coupled to a global current-summing node, said exposure transistor having a control gate coupled to a saturation level control voltage, said exposure transistor and said source-follower transistor acting as a common source differential pair to divide said bias current between said source-follower transistor and said exposure transistor according to the relative values of integrated photo signal on said control gate of said source-follower transistor and said saturation level control voltage on said gate of said exposure transistor.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance.

## Conclusion

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Lin Ye whose telephone number is (703) 305-3250. The examiner can normally be reached on Mon-Fri.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Andrew B Christensen can be reached on (703) 308-9644. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Art Unit: 2615

Lin Ye October 29, 2004

ANDREW CHRISTENSEN
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2600